IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned

Examiner: To Be Assigned

In re PATENT APPLICATION of

Applicant(s) : Yoshinori SHIZUNO

Appln. No. : To Be Assigned

: November 28, 2003 Filed

INFORMATION DISCLOSURE

STATEMENT

For

: SEMICONDUCTOR DEVICE WITH IMPROVED)

DESIGN FREEDOM OF EXTERNAL TERMINAL)

Atty. Dkt.

: OHG 146

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b) (1).

Attached is a copy of Japanese-language publication. The relevance of this publication can be gleaned from the attached English-language Abstract, and from page 2 of the specification. The publication is listed on the attached Form PTO-1449.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the documents submitted. Consideration of the submitted documents is respectfully requested.

Respectfully submitted,

November 28, 2003

Date

Steven M. Rabin (Reg. No. 29,102)

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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				Atty Docket	Application	Application No. To Be Assigned		
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				Applicant Yoshinori SHIZUNO				
				Filing Date November 28, 2003	1 '	Group Unit To Be Assigned		
			U.S.	PATENT DOCUMENTS				
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